

STATEMENT UNDER 37 CFR 1.97(e) ACCOMPANYING
INFORMATION DISCLOSURE STATEMENT

Docket No.
NEC NEG-298

In Re Application Of: TAKAHASHI et al.

Application No.	Filing Date	Examiner	Customer No.	Group Art Unit	Confirmation No.
10/620,659	July 16, 2003	TAN, VIBOL	27667	2819	1434

Inventor: Buffer Circuit, Buffer Tree, and Semiconductor Device

COMMISSIONER FOR PATENTS:

This is a statement under the provisions of 37 CFR 1.97(e) in the above-identified application.

Check applicable statement herebelow:

Statement Under 37 CFR 1.97(e)(1)

- ☒ Each item of information contained in the accompanying Information Disclosure Statement was first cited in any communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of the Information Disclosure Statement.

Statement Under 37 CFR 1.97(e)(2)

- ☐ No item of information contained in the accompanying Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart foreign application, and, to the knowledge of the undersigned person, after making reasonable inquiry, no item of information contained in the accompanying Information Disclosure Statement was known to any individual designated in 37 CFR 1.56(c) more than three months prior to the filing of the Information Disclosure Statement.


Signature

Dated: March 22, 2005

Norman P. Soloway
Registration No. 24,315
HAYES SOLOWAY P.C.
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Certificate of Mailing by First Class Mail

I certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to "Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450" [37 CFR 1.8(a)] on

March 22, 2005

(Date)


Signature of Person Mailing Correspondence

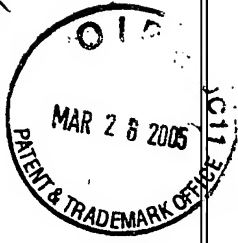
Kathryn Grimes

Typed or Printed Name of Person Mailing Correspondence

CC:

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AF Hew



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

APPLN. OF: TAKAHASHI et al.
SERIAL NO.: 10/620,659
FILED: July 16, 2003
FOR: Buffer Circuit, Buffer Tree, and Semiconductor Device
GROUP: 2819
EXAMINER: TAN, VIBOL
DOCKET: NEC NEG-298

MAIL STOP AF
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Dear Sir:

In connection with the above-entitled matter, Applicants hereby attach U.S. Patent Office form PTO-1449, including copies of the prior art references listed therein. English language abstracts are provided for Examiner's convenience. The references were cited in the Korean Office Action dated January 27, 2005 of the underlying Korean patent application. Enclosed is a copy of the Korean Office Action, together with an English translation of the relevant portions thereof. The claims in the present application are believed to be patentably distinguished over these references.

This information disclosure statement is being made pursuant to the duty of disclosure imposed by law and formulated in 37 CFR 1.56(A). No representation is made that the

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information thus disclosed in fact constitutes prior art or that it is the closest prior art, inasmuch as 37 CFR 1.56(A) relies on a materiality concept which depends on subjectivity.

In compliance with the requirements of 37 C.F.R. § 1.98(a)(3), as a concise statement of relevance, as it is presently understood by the individual designed in 35 U.S.C. § 1.56(c) most knowledgeable about the content of the information, the undersigned attorney of record submits a translation of portions of an official action by a foreign examiner in which the references were cited. The relevance to the pending U.S. patent application is that the references were cited in a foreign patent application on the same subject matter. However, no independent analysis of the reference, the accuracy of the statement of foreign examiner or the claims of the foreign application under the laws of the country or the United States relative to the subject matter claimed in the present application has been made; the present understanding of the contents thereof by the undersigned being based on the translation of the foreign examiner's comments submitted therewith.

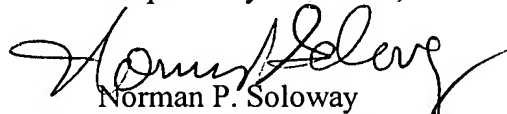
The enclosed Information Disclosure Statement is being submitted within three months of the Korean Office Action, as certified in the attached Certification for Information Disclosure Statement.

Form PTO-2038 authorizing credit card payment in the amount of \$180.00 to cover the fee under 37 CFR 1.97(d) is enclosed. In the event there are any fee deficiencies or additional fees are payable, please credit them, or credit any overpayment, to our Deposit Account No. 08-1391.

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Respectfully submitted,


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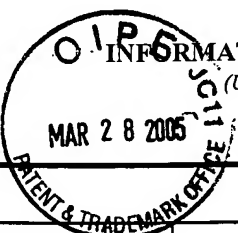
CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service as First Class Mail in an envelope addressed to: MAIL STOP AF, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on March 22, 2005 at Tucson, Arizona.

By

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				Docket Number (Optional) NEC NEG-298		Application Number 10/620,659		
				Applicant(s) TAKAHASHI et al.				
				Filing Date July 16, 2003		Group Art Unit 2819		
U.S. PATENT DOCUMENTS								
*EXAMINER INITIAL	REF	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE	
U.S. PATENT APPLICATION PUBLICATIONS								
*EXAMINER INITIAL	REF	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE	
FOREIGN PATENT DOCUMENTS								
	REF	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	Translation YES NO	
		11-353066	12.24.99	JAPAN (w/abstract)	H06F	3/00		
		05-048410	2.26.93	JAPAN (w/abstract)	H03K	5/01		
OTHER DOCUMENTS <i>(Including Author, Title, Date, Pertinent Pages, Etc.)</i>								
EXAMINER				DATE CONSIDERED				
EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP Section 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.								